

# ELEN90046 Major Design Project

<b>Credit Points:</b>	25															
<b>Level:</b>	9 (Graduate/Postgraduate)															
<b>Dates &amp; Locations:</b>	2015, Parkville This subject commences in the following study period/s: Semester 1, Parkville - Taught on campus. Semester 2, Parkville - Taught on campus.															
<b>Time Commitment:</b>	Contact Hours: 12 hours Total Time Commitment: 400 hours															
<b>Prerequisites:</b>	Prerequisites for this subject are: <table border="1" data-bbox="389 573 1485 891"> <thead> <tr> <th>Subject</th> <th>Study Period Commencement:</th> <th>Credit Points:</th> </tr> </thead> <tbody> <tr> <td>ELEN90042 Analogue Electronics</td> <td>Semester 1</td> <td>12.50</td> </tr> <tr> <td>ELEN90047 Mixed Signal Design</td> <td>Semester 1</td> <td>12.50</td> </tr> <tr> <td>ELEN90050 RF Systems and Architecture</td> <td>Semester 2</td> <td>12.50</td> </tr> <tr> <td>ELEN90049 RF Electronics and Design</td> <td>Semester 2</td> <td>12.50</td> </tr> </tbody> </table>	Subject	Study Period Commencement:	Credit Points:	ELEN90042 Analogue Electronics	Semester 1	12.50	ELEN90047 Mixed Signal Design	Semester 1	12.50	ELEN90050 RF Systems and Architecture	Semester 2	12.50	ELEN90049 RF Electronics and Design	Semester 2	12.50
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ELEN90050 RF Systems and Architecture	Semester 2	12.50														
ELEN90049 RF Electronics and Design	Semester 2	12.50														
<b>Corequisites:</b>	None															
<b>Recommended Background Knowledge:</b>	None															
<b>Non Allowed Subjects:</b>	None															
<b>Core Participation Requirements:</b>	For the purposes of considering request for Reasonable Adjustments under the Disability Standards for Education (Cwth 2005), and Students Experiencing Academic Disadvantage Policy, academic requirements for this subject are articulated in the Subject Description, Subject Objectives, Generic Skills and Assessment Requirements of this entry. The University is dedicated to provide support to those with special requirements. Further details on the disability support scheme can be found at the Disability Liaison Unit website: <a href="http://www.services.unimelb.edu.au/disability/">http://www.services.unimelb.edu.au/disability/</a>															
<b>Coordinator:</b>	Prof Stan Skafidas															
<b>Contact:</b>	Prof Stan Skafidas Email: <a href="mailto:sskaf@unimelb.edu.au">sskaf@unimelb.edu.au</a> ( <a href="mailto:sskaf@unimelb.edu.au">mailto:sskaf@unimelb.edu.au</a> )															
<b>Subject Overview:</b>	<p><b>AIMS</b></p> <p>The aim of this subject is to get students to apply their knowledge and skills learnt in the course in designing a real system.</p> <p><b>INDICATIVE CONTENT</b></p> <p>This subject centres on a group design project where students design and fabricate a substantial electronics system (example applications include wireless, GPS, radar). The final mark will also take into account the methodologies used, testing, simulation and verification of the designed system and adherence to appropriate standards.</p>															
<b>Learning Outcomes:</b>	<p><b>INTENDED LEARNING OUTCOMES (ILO)</b></p> <p>Having completed this subject it is expected that the student be able to:</p> <ol style="list-style-type: none"> <li>1 Design a substantial electronics system</li> <li>2 Apply their knowledge and skills learnt in the course in designing a real system.</li> </ol>															

<b>Assessment:</b>	A written report of up to 100 pages due at the end of semester (approximately 300-350 hours of work), worth 50% An oral examination of duration 60 minutes at the end of semester (50%). Intended Learning Outcomes (ILOs) 1 and 2 are assessed in the submitted final report and oral examination
<b>Prescribed Texts:</b>	None
<b>Breadth Options:</b>	This subject is not available as a breadth subject.
<b>Fees Information:</b>	Subject EFTSL, Level, Discipline & Census Date, <a href="http://enrolment.unimelb.edu.au/fees">http://enrolment.unimelb.edu.au/fees</a>
<b>Generic Skills:</b>	<ul style="list-style-type: none"> <li># Ability to apply knowledge of science and engineering fundamentals</li> <li># Ability to undertake problem identification, formulation, and solution</li> <li># Ability to utilise a systems approach to complex problems and to design and operational performance</li> <li># Ability to build and test real world systems that meet industry specialisation and manufacturing standards</li> <li># Proficiency in engineering design</li> <li># Ability to conduct an engineering project</li> <li># Capacity for creativity and innovation</li> <li># Understanding of professional and ethical responsibilities, and commitment to them</li> <li># Ability to function effectively as an individual and in multidisciplinary and multicultural teams, as a team leader or manager as well as an effective team member</li> <li># Capacity for lifelong learning and professional development</li> </ul>
<b>Related Course(s):</b>	Master of Nanoelectronic Engineering