ELEN90049 RF Electronics and Design

Credit Points:	12.50
Level:	9 (Graduate/Postgraduate)
Dates & Locations:	2010, Parkville This subject commences in the following study period/s: Semester 2, Parkville - Taught on campus.
Time Commitment:	Contact Hours: 1 two hour lecture per week Total Time Commitment: 120 hours
Prerequisites:	Prerequisites for this subject are

Subject	Study Period Commencement:	Credit Points:
ELEN90043 Device Models	Semester 1	12.50
ELEN90048 Passive Component Design & Simulation	Semester 1	12.50

Corequisites:	Corequisite for this subject is

Subject	Study Period Commencement:	Credit Points:
ELEN90050 RF Systems and Architecture	Semester 2	12.50

Recommended Background Knowledge:	None
Non Allowed Subjects:	None
Core Participation Requirements:	For the purposes of considering request for Reasonable Adjustments under the Disability Standards for Education (Cwth 2005), and Students Experiencing Academic Disadvantage Policy, academic requirements for this subject are articulated in the Subject Description, Subject Objectives, Generic Skills and Assessment Requirements of this entry. The University is dedicated to provide support to those with special requirements. Further details on the disability support scheme can be found at the Disability Liaison Unit website: http://www.services.unimelb.edu.au/disability/
Coordinator:	Prof Stan Skafidas
Contact:	Melbourne School of Engineering Office Building 173, Grattan Street The University of Melbourne VIC 3010 Australia General telephone enquiries + 61 3 8344 6703 + 61 3 8344 6507 Facsimiles + 61 3 9349 2182 + 61 3 8344 7707 Email **eng-info@unimelb.edu.au (mailto:eng-info@unimelb.edu.au)**
Subject Overview:	This subject will introduce students to high frequency design of low noise amplifiers, mixers, voltage controlled oscillators, power amplifiers, power combining techniques, Doherty power amplifiers, stacked transistor designs, dividers and phase locked loops. After completing this subject students will be able to design, simulate (schematic and post layout simulation), extract and fabricate components operating in the 50+GHz frequency range.
Objectives:	Upon successful completion of this subject students should be able to: # Design, simulate and analyse the performance of RF low noise amplifiers, on chip power amplifiers, up and down conversion mixers, Voltage Controlled Oscillators and Phase Locked Loops.

Assessment:	One, written examination (not exceeding three hours) at the end of semester, worth 70%; Continuous assessment of submitted project work (not exceeding 30 pages in total over the semester), worth 30%.
Prescribed Texts:	TBA
Breadth Options:	This subject is not available as a breadth subject.
Fees Information:	Subject EFTSL, Level, Discipline & Census Date, http://enrolment.unimelb.edu.au/fees
Generic Skills:	# Ability to apply knowledge of science and engineering fundamentals # Ability to undertake problem identification, formulation, and solution # Ability to utilise a systems approach to complex problems and to design andoperationalperformance # Ability to build and test real world systems that meet industry specialisation and manufacturing standards # Capacity for lifelong learning and professional development
Related Course(s):	Master of Nanoelectronic Engineering