

ELEN40013 Electronic Circuit Design 3

Credit Points:	12.50
Level:	4 (Undergraduate)
Dates & Locations:	2010, Parkville This subject commences in the following study period/s: Semester 1, Parkville - Taught on campus.
Time Commitment:	Contact Hours: Twenty-four hours of lectures, 6 hours of tutorials, 18 hours of assignments/ project work Total Time Commitment: 120 hours
Prerequisites:	431-331 Electronic Circuit Design 2
Corequisites:	None
Recommended Background Knowledge:	None
Non Allowed Subjects:	None
Core Participation Requirements:	For the purposes of considering request for Reasonable Adjustments under the Disability Standards for Education (Cwth 2005), and Students Experiencing Academic Disadvantage Policy, academic requirements for this subject are articulated in the Subject Description, Subject Objectives, Generic Skills and Assessment Requirements of this entry. The University is dedicated to provide support to those with special requirements. Further details on the disability support scheme can be found at the Disability Liaison Unit website: http://www.services.unimelb.edu.au/disability/
Coordinator:	Assoc Prof Graeme Pendock
Contact:	Melbourne School of Engineering Office Building 173, Grattan Street The University of Melbourne VIC 3010 Australia General telephone enquiries + 61 3 8344 6703 + 61 3 8344 6507 Facsimiles + 61 3 9349 2182 + 61 3 8344 7707 Email eng-info@unimelb.edu.au (mailto:eng-info@unimelb.edu.au)
Subject Overview:	This course will build on 431-331 Electronic Circuit Design 2. Students completing it will gain skill for designing practical electronic circuits and exposure to some of the techniques and tools involved in the design, manufacture and assembly of real-world electronic circuits. Topics covered: circuit fundamentals (eg. noise in electronic circuits and systems, high frequency design), sub-systems (eg. crystal oscillators phase-locked loops and frequency synthesisers), trade-offs in electronic design (eg. illustrating topics such as analog vs digital approaches, component precision vs robust design), component technologies (leaded vs SMD, parasitics, real behaviour of passives and their choice), PCB design techniques (CAD, multilayer boards, layout and routing for EMI, power distribution, high speed and mixed signal), electronic manufacturing (PCBs, component loading and soldering techniques).
Objectives:	On completing this subject the student should be able to: <ul style="list-style-type: none"> # Apply practical knowledge of a range of common electrical and electronic devices and circuit techniques; # Identify choices in implementing a design for a given problem and make tradeoffs on the basis of the relative merits of different approaches; # Prototype hardware, debug hardware and software, conduct measurements and tests.

Assessment:	One 3-hour end-of-semester written examination (60%); 1-hour mid-semester examination (10%); Laboratory/assignment reports due throughout the semester (30%).
Prescribed Texts:	None
Breadth Options:	This subject is not available as a breadth subject.
Fees Information:	Subject EFTSL, Level, Discipline & Census Date, http://enrolment.unimelb.edu.au/fees
Generic Skills:	<ul style="list-style-type: none"> # Understanding the need for trade-offs in design # Appreciation of the practical limitations in real-world components # Appreciation of issues in manufacturing # Appreciation of importance on robustness in design and the impact it has on manufacturability
Related Course(s):	Bachelor of Engineering (Computer Engineering) Bachelor of Engineering (Electrical Engineering) Bachelor of Engineering (Electrical) and Bachelor of Arts Bachelor of Engineering (Electrical) and Bachelor of Commerce Bachelor of Engineering (Electrical) and Bachelor of Laws Bachelor of Engineering (Electrical) and Bachelor of Science Bachelor of Engineering (EngineeringManagement) Electrical Bachelor of Engineering (IT) Computer Engineering Bachelor of Engineering (IT) Electrical Engineering Bachelor of Engineering (Software Engineering) Postgraduate Certificate in Engineering